



PATENT

IN THE UNITED STATES PATENT OFFICE

Applicant: Stephen M. Trimberger
Assignee: Xilinx, Inc.
Title: "Methods for Using Defective Programmable Logic Devices by Customizing Designs Based on Recorded Defects"
Serial No.: 10/085,305 File Date: 02-28-02
Examiner: James C. Kerveros Art Unit: 2133
Docket No.: X-874 US Conf. No.: 7532

Mail Stop AMENDMENT
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P.O. Box 1450
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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicant brings to the attention of the Examiner the twenty-six (26) references listed in the attached Substitute for Form 1449A/PTO. A copy of three (3) of the references are enclosed herein.

The enclosed reference(s) is/are being cited after the receipt of a first office action but prior to any final action.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

The Commissioner is hereby authorized to charge the fee of \$180.00 due under 1.17(p) and any additional fees, or credit any overpayment which may be required to Deposit Account No. 24-0040.

Respectfully submitted,

Lois D. Cartier
Agent for Applicant
Reg. No. 40,941

11/30/2004 EAREGAY1 00000040 240040 10085305

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*I hereby certify that this correspondence is being deposited with the United States Postal Service as **first class mail** in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on November 23, 2004.*

Pat Slaback
Name

Signature

LDC : pbs

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet	1	of	3
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Complete if Known

Application / Conf. No.	10/085,305 / 7532
Filing Date	February 28, 2002
First Named Inventor	Stephen M. Trimberger
Art Unit	2133
Examiner Name	James C. Kerveros
Attorney Docket Number	X-874 US

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code (if known)			
		US- 4,899,067	02-06-90	So et al.	
		US- 5,153,880	10-06-92	Owen et al.	
		US- 5,255,227	10-19-93	Haeffele	
		US- 5,349,248	09-20-94	Goetting et al.	
		US- 5,459,342	10-17-95	Nogami et al.	
		US- 5,485,102	01-16-96	Cliff et al.	
		US- 5,498,975	03-12-96	Cliff et al.	
		US- 5,513,144	04-30-96	O'Toole	
		US- 5,592,102	01-07-97	Lane et al.	
		US- 5,742,556	04-21-98	Tavrow et al.	
		US- 5,764,577	06-09-98	Johnston et al.	
		US- 5,777,887	07-07-98	Marple et al.	
		US- 6,166,559	12-26-00	McClintock et al.	
		US- 6,292,925 B1	09-18-01	Dellinger et al.	
		US- 6,344,755	02-05-02	Reddy et al.	
		US- 6,496,971 B1	12-17-02	Lesea et al.	
		US- 6,560,740	05-06-03	Zuraski Jr. et al.	
		US- 6,664,808 B2	12-16-03	Ling et al.	
		US- 10/085,305	02-28-02	Trimberger	
		US- 10/714,380	10-31-03	Trimberger	

FOREIGN PATENT DOCUMENTS

[illegible]Examiner
SignatureDate
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Examiner Name	James C. Kerveros
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Substitute for form 1449A/PTO		Complete if Known			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application / Conf. No.	10/085,305 / 7532		
		Filing Date	February 28, 2002		
		First Named Inventor	Stephen M. Trimberger		
		Art Unit	2133		
		Examiner Name	James C. Kerveros		
Sheet	3	of	3	Attorney Docket Number	X-874 US

OTHER -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		John Emmert et al.; "Dynamic Fault Tolerance in FPGAs via Partial Reconfiguration"; Annual IEEE Symposium on Field-Programmable Custom Computing Machines; 17 April 2000; pp. 165-174.	
		John M. Emmert et al.; "Incremental Routing in FPGAs"; ASIC Conference 1998. Proceedings, Eleventh Annual IEEE International; Rochester, NY; 13-16 September 1998; pp. 217-221.	
		Altera; Altera Data Sheet, Vol. 1, Chapter 3 "Configuration and Testing"; and Vol. 2 Chapter 8 "Remote System Upgrades with Stratix II Devices"; February 2004; downloaded on June 17, 2004 from http://www.altera.com/literature/lit-stx2 .	

Examiner Signature		Date Considered	
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